Panasonic

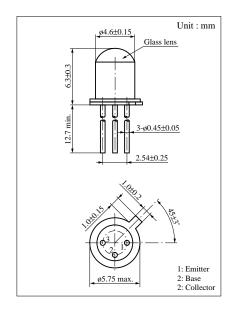
PNZ102 (PN102)

Silicon NPN Phototransistor

For optical control systems

Features

- High sensitivity
- Wide spectral sensitivity, suited for detecting GaAs LEDs
- Low dark current : $I_{CEO} = 5 \text{ nA (typ.)}$
- Fast response : t_r , $t_f = 3 \mu s$ (typ.)
- Base pin for easy circuit design
- TO-18 standard type package



■ Absolute Maximum Ratings (Ta = 25°C)

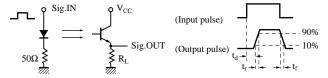
Parameter	Symbol	Ratings	Unit	
Collector to emitter voltage	V _{CEO}	30	V	
Collector to base voltage	V_{CBO}	40	V	
Emitter to collector voltage	V _{ECO}	5	V	
Emitter to base voltage	V_{EBO}	5	V	
Collector current	I_C	50	mA	
Collector power dissipation	P _C	150	mW	
Operating ambient temperature	T _{opr}	-25 to +85	°C	
Storage temperature	T_{stg}	-30 to +100	°C	

■ Electro-Optical Characteristics (Ta = 25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Dark current	I_{CEO}	$V_{CE} = 10V$		5	300	nA
Collector photo current	I _{CE(L)}	$V_{CE} = 10V, L = 100 lx^{*1}$	1.5	3.5		mA
Peak sensitivity wave length	λ_{P}	$V_{CE} = 10V$		800		nm
Acceptance half angle	θ	Measured from the optical axis to the half power point		10		deg.
Response time	t _r , t _f *2	$V_{CC} = 10V, I_{CE(L)} = 5mA, R_L = 100\Omega$		3		μs
Collector saturation voltage	V _{CE(sat)}	$I_{CE(L)} = 1 \text{mA}, L = 500 \text{ lx}^{*1}$		0.2	0.4	V

^{*1} Measurements were made using a tungsten lamp (color temperature T = 2856K) as a light source.

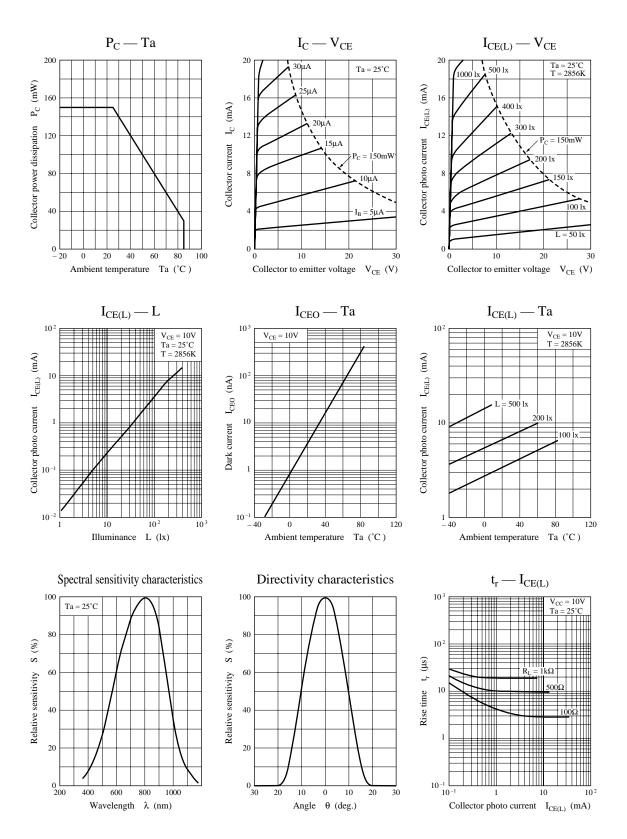
^{*2} Switching time measurement circuit



- t_d: Delay time
- t_r: Rise time (Time required for the collector photo current to increase from 10% to 90% of its final value)
- t_f: Fall time (Time required for the collector photo current to decrease from 90% to 10% of its initial value)

Note) The part number in the parenthesis shows conventional part number.

Phototransistors PNZ102



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